In the Claims

1 (Canceled)	1	1. (Canceled).
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- 2. (Amended) A method operable on a clocking system that includes a plurality of tiers
- 2 of clock dividers that successively divide a reference frequency derived from a master
- 3 clock frequency for producing a plurality of desired frequencies, comprising:
- 4 (A) determining a least common multiple (LCM) of the desired frequencies and the
- 5 master clock frequency;
- 6 (B) selecting divider values for one of the plurality of tiers of dividers subject to a
- 7 requirement that input frequencies to that tier of dividers fall within a predetermined
- 8 range and that they add a minimum number of few new factors to the LCM;
- 9 (C) multiplying the LCM by any new factors needed to realize the dividers of the selected
- 10 tier of dividers to yield a LumpLCM;
- 11 (D) repeating steps B and C for all except the last tier of dividers, including updating the
- 12 LumpLCM for each repetition to include any new factors needed to realize the dividers
- 13 for the respective tier; and A method as recited in claim 1, further comprising:
- 14 (E) computing values for the last tier of dividers responsive to LumpLCM and the
- 15 reference frequency.
 - 1 3. (Amended) A method as recited in claim 2, wherein the reference frequency is defined
 - 2 as a product of a master clock frequency and a K multiplier, and wherein the K multiplier
- 3 is variable for varying the reference frequency-can be varied by varying the K-multiplier.
- 4. (Original) A method as recited in claim 3, further comprising:
- determining an integer p such that p * LumpLCM falls within an allowable range
- 3 of the reference frequency; and
- computing the reference frequency as the product p * LumpLCM.
- 5. (Original) A method as recited in claim 4, further comprising:

- computing the K multiplier as the reference frequency divided by the master clock frequency.
- 6. (Original) A method as recited in claim 2, wherein the reference frequency is defined
- 2 as a product of a master clock frequency and a fixed K multiplier.
- 7. (Original) A method as recited in claim 6, further comprising:
- determining a least common multiple (BigLCM) of the desired frequencies and the reference frequency,
- wherein the step (B) of selecting divider values for one of the plurality of tiers of dividers is subject to a requirement that input frequencies add a minimum number of few new factors to BigLCM.
- 8. (Original) A method as recited in claim 7, further comprising:
- determining a real number n such that n * LumpLCM equals the reference frequency; rounding n to the nearest integer to yield n_r; and
- modifying the desired frequencies by a factor n_r/n to account for rounding errors introduced in the rounding step.
- 9. (Amended) A method as recited in claim 21, wherein the clocking system consists of two tiers of dividers.
- 1 10. (Amended) A method operable on a clocking system that includes a plurality of
- 2 tiers of clock dividers that successively divide a reference frequency, which is variable
- 3 over an allowable range, for producing a plurality of desired frequencies, the reference
- 4 frequency being defined as a product of a master clock frequency and a variable
- 5 multiplier K, the method comprising:
- 6 (A) determining a least common multiple (LCM) of the desired frequencies and the
- 7 master clock frequency;

- 8 (B) selecting divider values for one of the plurality of tiers of dividers subject to a
- 9 requirement that input frequencies to that tier of dividers fall within a predetermined
- range and that they add a minimum number of few new factors to the LCM;
- (C) multiplying the LCM by any new factors needed to realize the dividers of the selected
- 12 | tier of dividers to yield a new LCM; as LumpLCM;
- (D) determining an integer p such that p * LumpLCM falls within an allowable range of
- 14 the reference frequency; and
- 15 (E) computing the reference frequency as the product p * LumpLCM.
 - 1 11. (Original) A method as recited in claim 10, further comprising:
 - 2 repeating steps B and C for all except the last tier of dividers, including updating
 - 3 LumpLCM for each repetition to include any new factors needed to realize the dividers
 - 4 for the respective tier.
 - 1 12. (Original) A method as recited in claim 10, further comprising:
 - computing values for the last tier of dividers responsive to LumpLCM and the
 - 3 reference frequency.
 - 1 13. (Original) A method as recited in claim 12, further comprising:
 - computing the K multiplier as the reference frequency divided by the master clock
 - 3 frequency.
 - 1 14. (Original) A method as recited in claim 13, wherein the reference frequency is
 - defined as a product of a master clock frequency and a fixed K multiplier.
 - 1 15. (Original) A method operable on a clocking system that includes a plurality of tiers
 - of clock dividers that successively divide a fixed reference frequency for producing a
 - 3 plurality of desired frequencies, the reference frequency being defined as a product of a
 - 4 master clock and a multiplier K, the method comprising:

- 5 (A) determining a least common multiple (LCM) of the desired frequencies and the
- 6 master clock frequency;
- 7 (B) determining a least common multiple (BigLCM) of the desired frequencies and the
- 8 fixed reference frequency;
- 9 (C) selecting divider values for one of the plurality of tiers of dividers subject to a
- requirement that input frequencies to that tier fall within a predetermined range and that
- they add a minimum number of few new factors to BigLCM;
- (D) multiplying the LCM by any new factors needed to realize the dividers of the selected
- tier of dividers to yield a LumpLCM;
- (E) determining a real number n such that n * LumpLCM equals the reference frequency;
- (F) rounding n to the nearest integer to yield n_r ; and
- 16 (G) modifying the desired frequencies by a factor n_r/n to account for rounding errors
- introduced in step F.
- 1 16. (Original) A method as recited in claim 15, wherein the number of tiers of dividers is
- 2 two.
- 1 17. (Original) A method as recited in claim 15, further comprising, prior to step A,
- 2 attributing at least two of the desired frequencies to a coherency group, and performing
- 3 steps A-G using only the desired frequencies attributed to the coherency group.
- 1 18. (Original) A method as recited in claim 17, further comprising performing steps A-G
- 2 independently for different coherency groups.
- 1 19. (Original) A method as recited in claim 18, wherein coherency groups are user-
- 2 assignable.
- 20. (Original) A method as recited in claim 15, wherein the tier of dividers selected in
- 2 step C is the tier of dividers whose output produces the desired frequencies.